KSD15N06

60V N-Channel MOSFET

General Description

The KSD15N06 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

Product Summary

V_{DS} (V)	$R_{DS(on)}$ (mΩ)	I_D (A)
60	36 at V_{GS} = 10 V	15
	43 at V_{GS} = 4.5 V	10

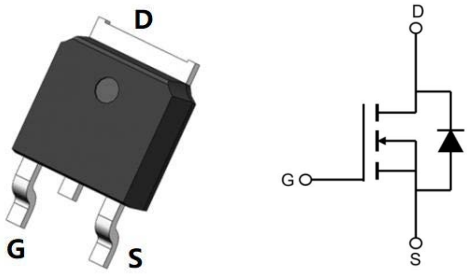
Features

- High density cell design for ultra low R_{dson}
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

TO-252 Pin Configuration



Absolute Maximum Ratings **Tc=25℃ unless otherwise noted**

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current – Continuous (T_C=25℃)	15	A
	Drain Current – Continuous (T_C=100℃)	8	A
I_{DM}	Drain Current – Pulsed[1]	50	A
P_D	Power Dissipation (T_C=25℃)	20	W
	Power Dissipation (T_C=100℃)	0.86	W/℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	61	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.48	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	60	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=55V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=55V , V_{GS}=0V , T_J=125℃	---	---	10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=7A	---	36	43	mΩ
		V_{GS}=4.5V , I_D=5A	---	43	51	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.0	1.8	3	V
gfs	Forward Transconductance	V_{DS}=10V , I_S=5A	---	17	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	V_{DS}=30V , V_{GS}=10V , I_D=7A	---	7.5	---	nC
Q_{gs}	Gate-Source Charge		---	4.1	---	
Q_{gd}	Gate-Drain Charge		---	8.3	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DS}=30V, R_L=3Ω, V_{GS}=10V, R_G=3Ω	---	9	---	ns
T_r	Rise Time		---	21	---	
$T_{d(off)}$	Turn-Off Delay Time		---	14	---	
T_f	Fall Time		---	18	---	
C_{iss}	Input Capacitance	V_{DS}=30V , V_{GS}=0V , F=1MHz	---	670	---	pF
C_{oss}	Output Capacitance		---	50	---	
C_{rss}	Reverse Transfer Capacitance		---	17	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	12	A
I_{SM}	Pulsed Source Current		---	---	25	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1.0	V

Note :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition:Tj=25℃,V_{DD}=30V,V_G=10V,L=0.5mH,Rg=25Ω

Typical Electrical and Thermal Characteristics (Curves)

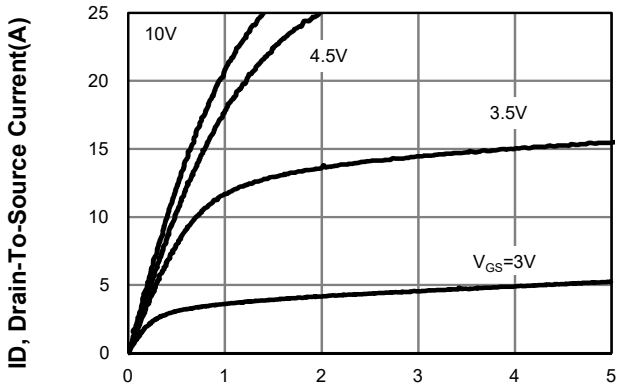


Fig1:Output Characteristics

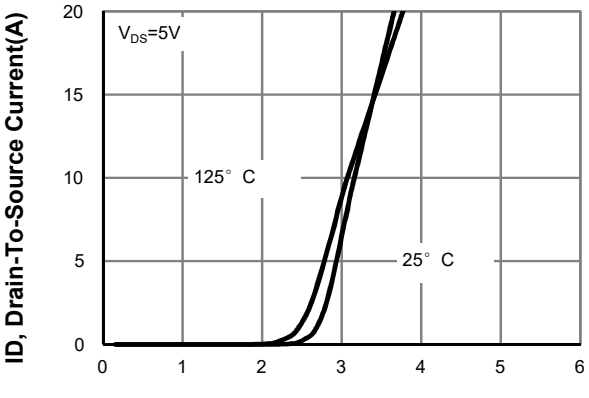


Fig2:Transfer Characteristics

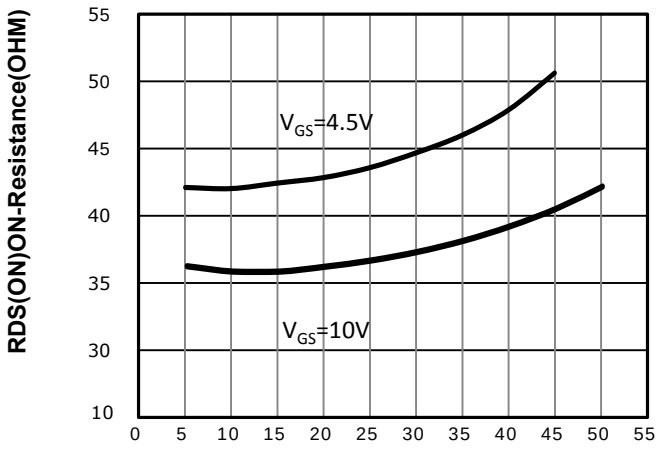


Fig3:Rdson- Drain Current

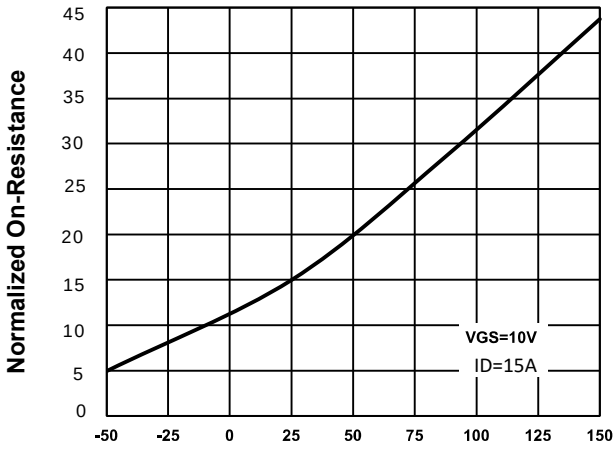


Fig4:Rds(on) vs. Temperature

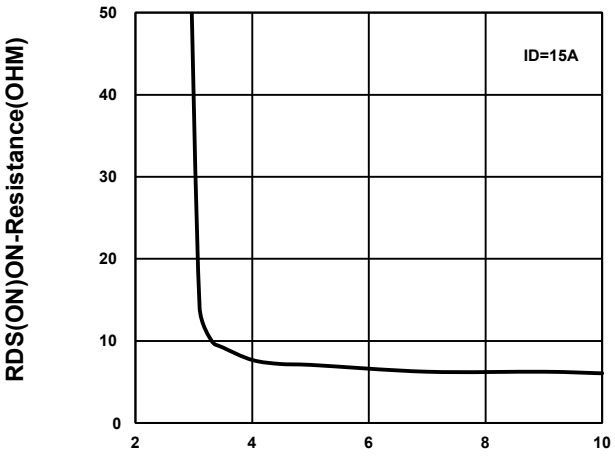


Figure 5:On-Resistance VS Gate-To-Source Voltage

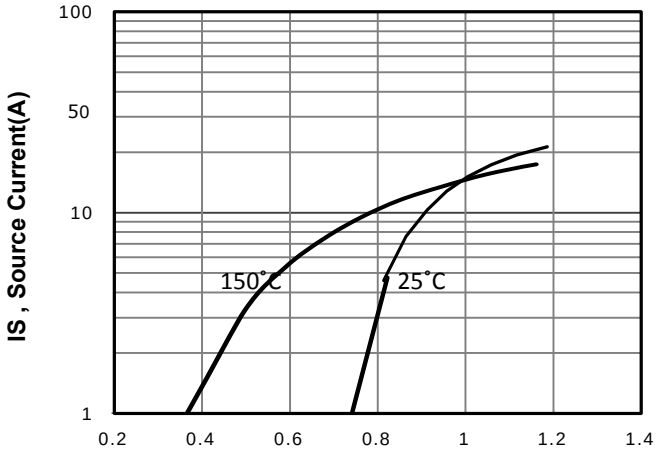


Figure 6: Source- Drain Diode Forwrd

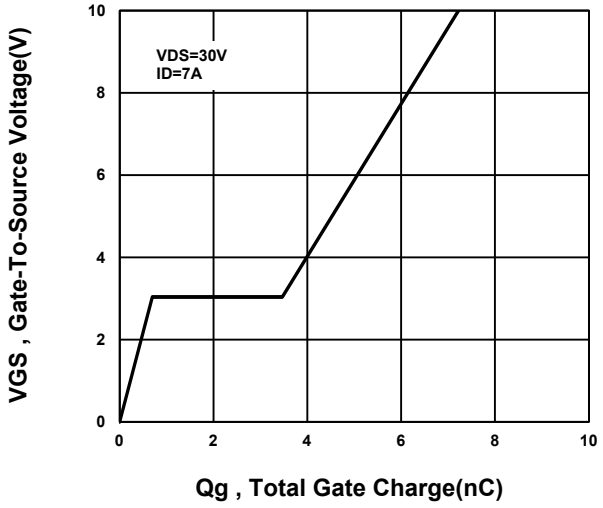


Figure 7: Gate-Charge Characteristics

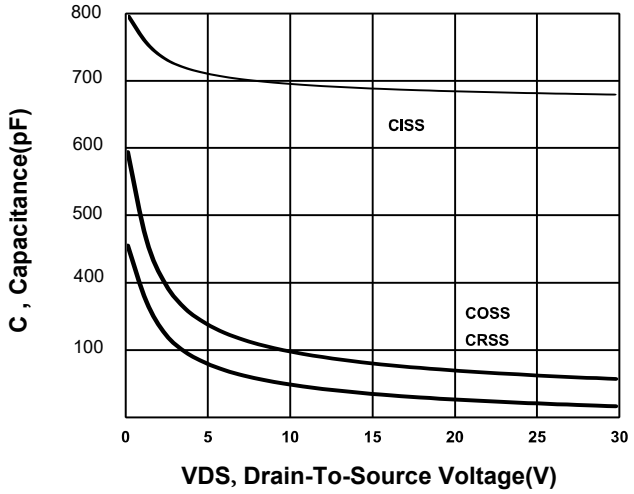


Figure 8: Capacitance vs Vds

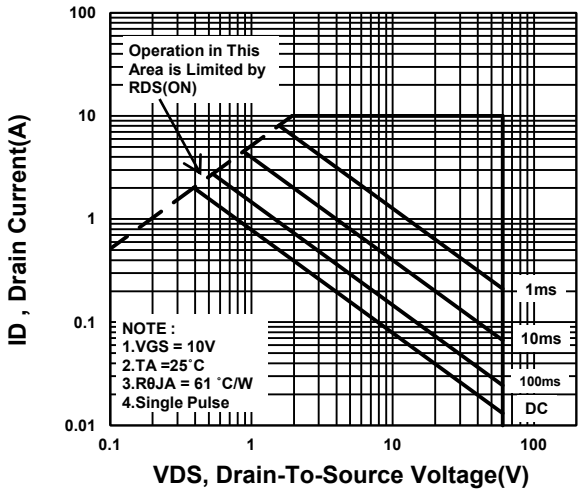


Figure 9: Safe Operating Area

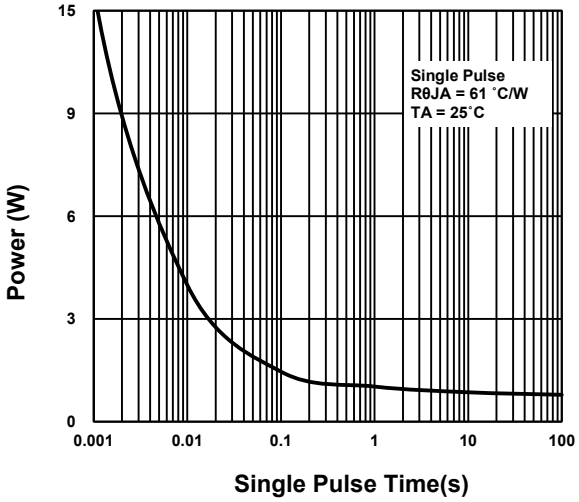


Figure 15: Single Pulse Maximum Power

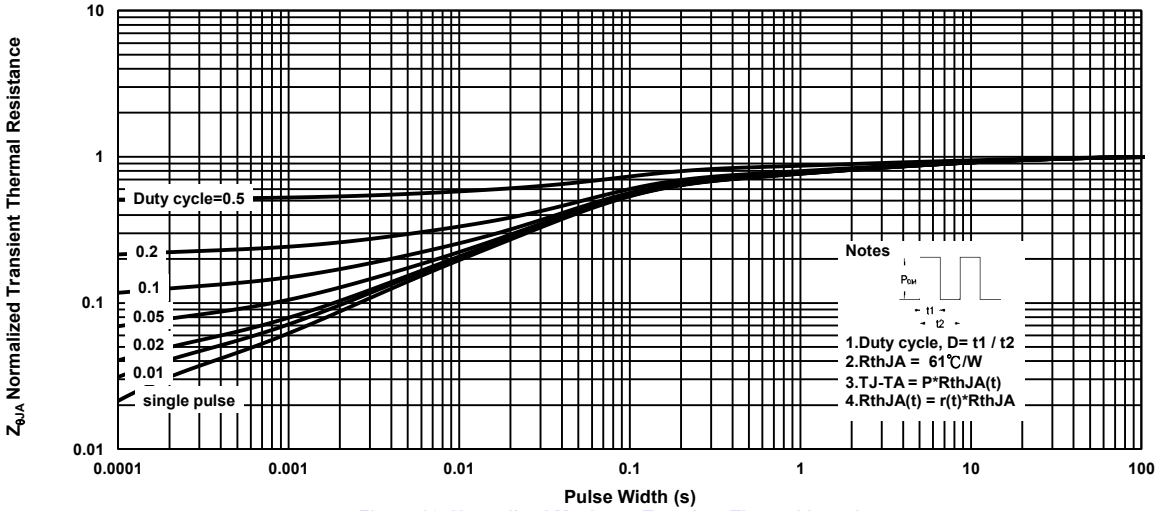


Figure 16: Normalized Maximum Transient Thermal Impedance

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