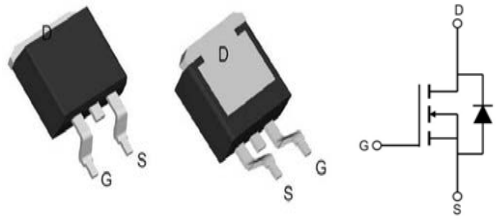
KSF280N10

100V N-Channel MOSFET

General Description

The KSF280N10 power MOSFET is produced with advanced technology of KORSUN.This technology enable the power MOSFET to have berrer characteristics,including fast switching time,low on resistance,low gate charge and especially excellent avalanche characteristics.

TO-263 Pin Configuration



Product Summary

V_{DS} (V)	$R_{DS(on)}$ (mΩ)	I_D (A)
100	1.8 at V_{GS} = 10V	280

Features

- Fully characterized avalanche voltage and current
- Special process technology for high ESD capability
- Good stability and uniformity with high EAS

Applications

- Power switching application
- Hard switched and high frequency circuits
- Power Management

Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current – Continuous (T_C=25℃)	280	A
	Drain Current – Continuous (T_C=100℃)	196	A
I_{DM}	Drain Current – Pulsed[1]	840	A
P_D	Power Dissipation (T_C=25℃)	320	W
	Power Dissipation (T_C=100℃)	0.65	W/℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	60	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.5	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	100	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=100V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=100V , V_{GS}=0V , T_J=125℃	---	---	30	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}= 10V， ID=20A	---	1.8	2.3	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	VGS = VDS, IDS= 250μA	3	3.5	4	V
gfs	Forward Transconductance	VDS= 5V, ID= 20A	---	60	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	VDS=50V , VGS=10V , ID=20A	---	180	---	nC
Q_{gs}	Gate-Source Charge		---	35	---	
Q_{gd}	Gate-Drain Charge		---	42	---	
$T_{d(on)}$	Turn-On Delay Time	VGS = 10V, VDS = 50V, RG = 6.0Ω	---	40	---	ns
T_r	Rise Time		---	72	---	
$T_{d(off)}$	Turn-Off Delay Time		---	140	---	
T_f	Fall Time		---	110	---	
C_{iss}	Input Capacitance	VDS=50V, VGS=0V, f=1.0MHz	---	9630	---	pF
C_{oss}	Output Capacitance		---	1615	---	
C_{rss}	Reverse Transfer Capacitance		---	80	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	280	A
I_{SM}	Pulsed Source Current		---	---	340	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1.2	V
trr	Body Diode Reverse Recovery Time	IF=20A, di/dt=500A/μs	---	80	---	ns
Qrr	Body Diode Reverse Recovery Charge		---	990	---	nC

Note :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition: TJ=25℃, VDD=50V, VG=10V, RG=25Ω, L=0.5mH, IAS=19A

Typical Electrical and Thermal Characteristics (Curves)

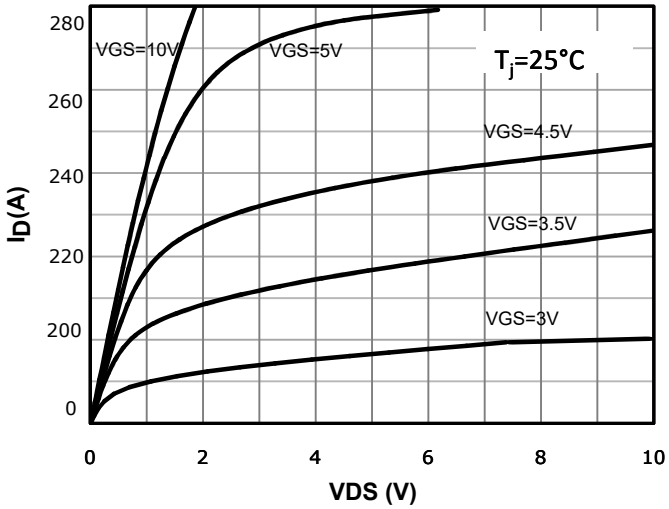


Fig1:Output Characteristics

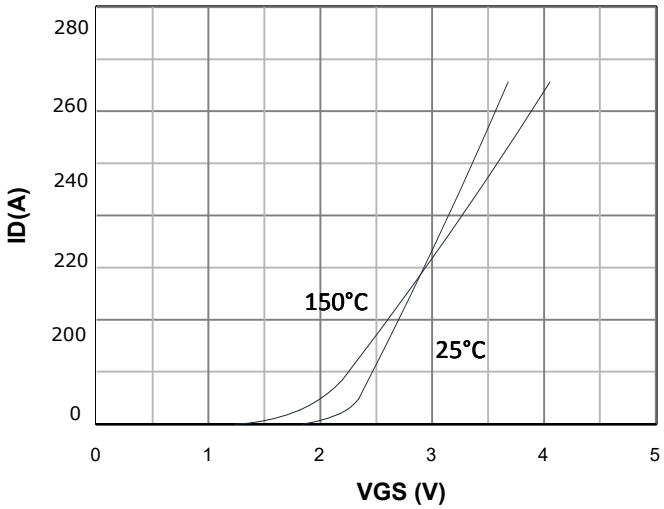


Fig2:Transfer Characteristics

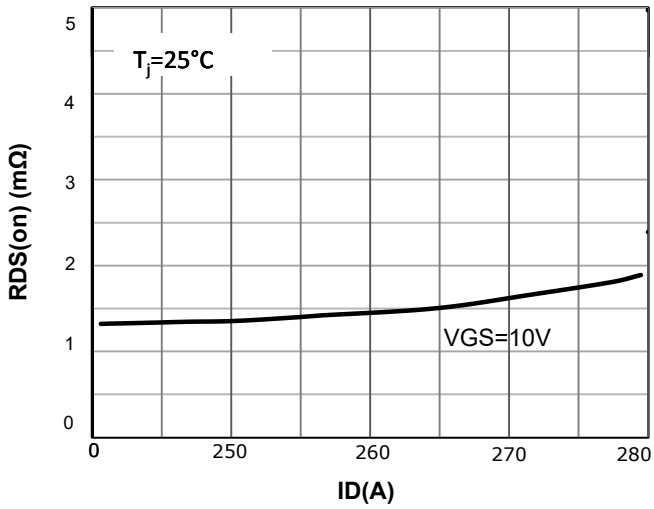


Fig3:Rdson- Drain Current

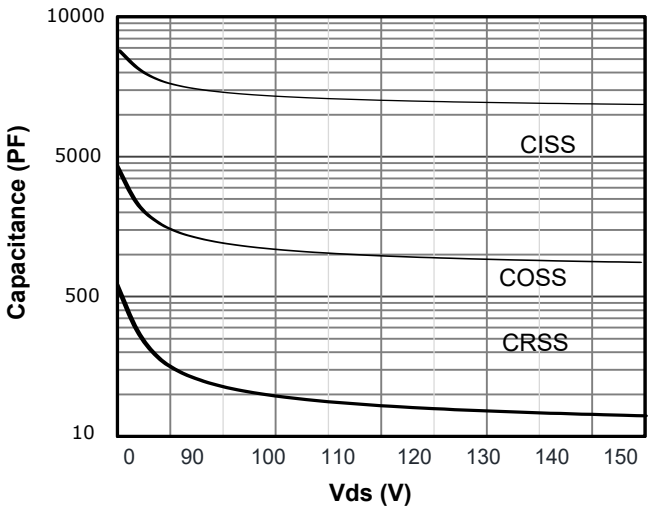


Fig4:Capacitance vs Vds

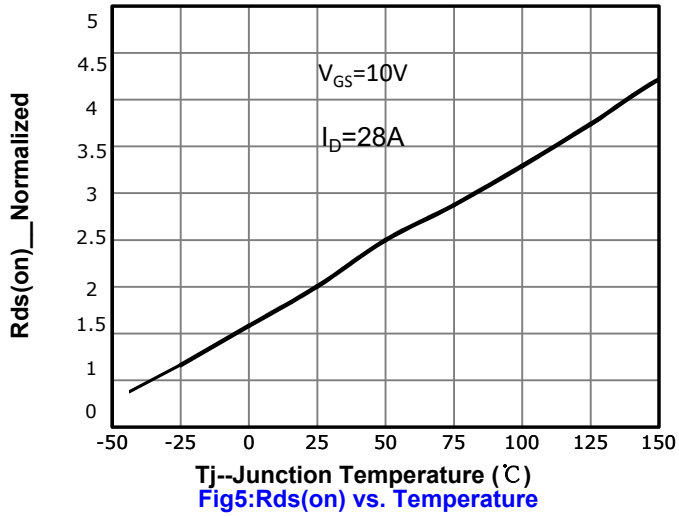


Fig5:Rds(on) vs. Temperature

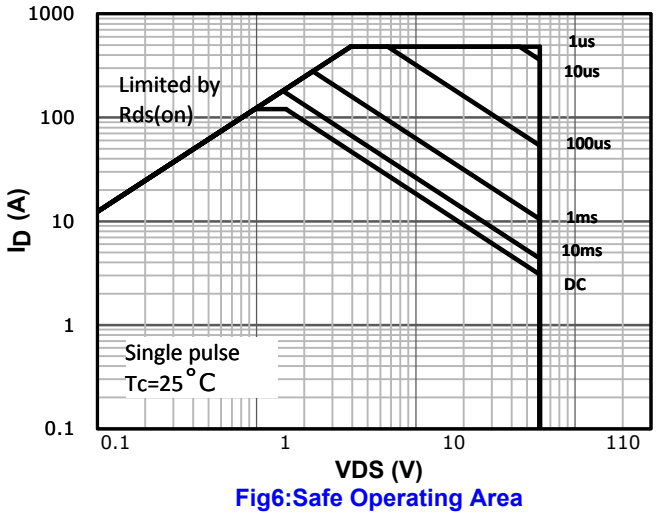


Fig6:Safe Operating Area

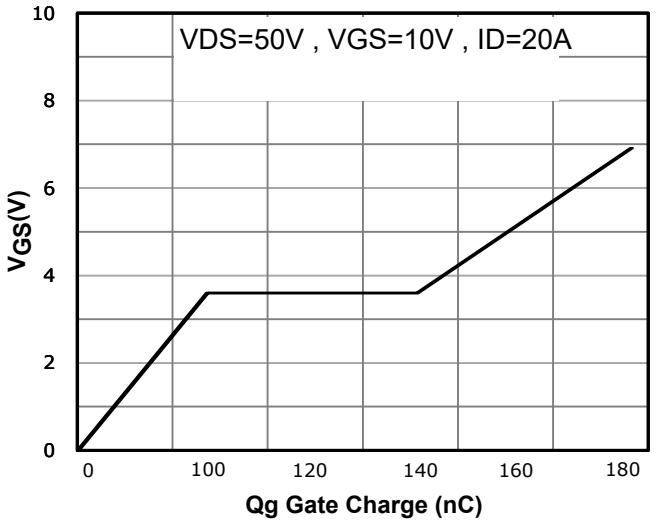


Fig7:Gate Charge

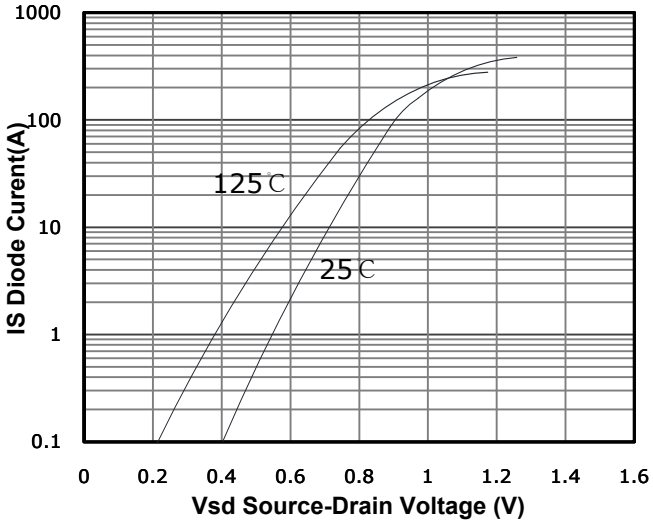


Fig8:Source- Drain Diode Forwrd

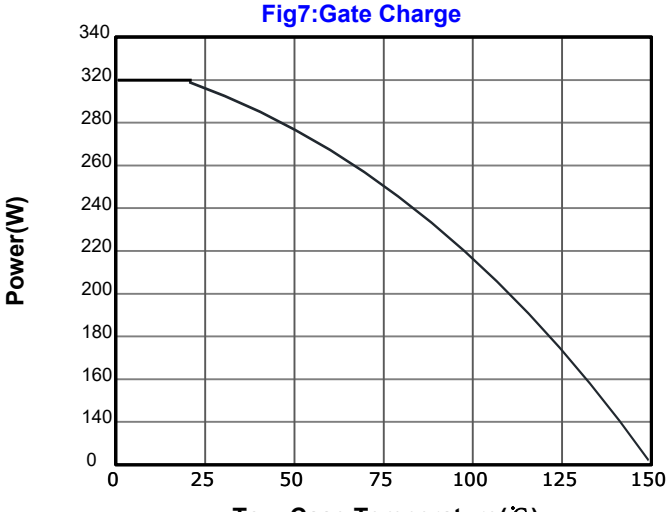


Fig9:Power Disipation

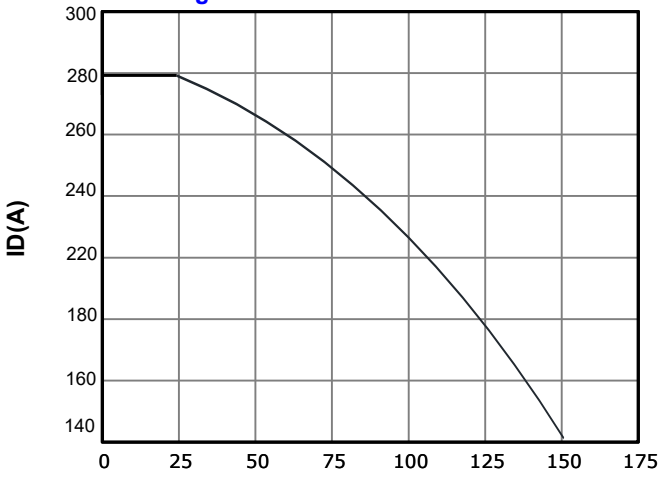


Fig10：IDCurrent- Junction Temperature

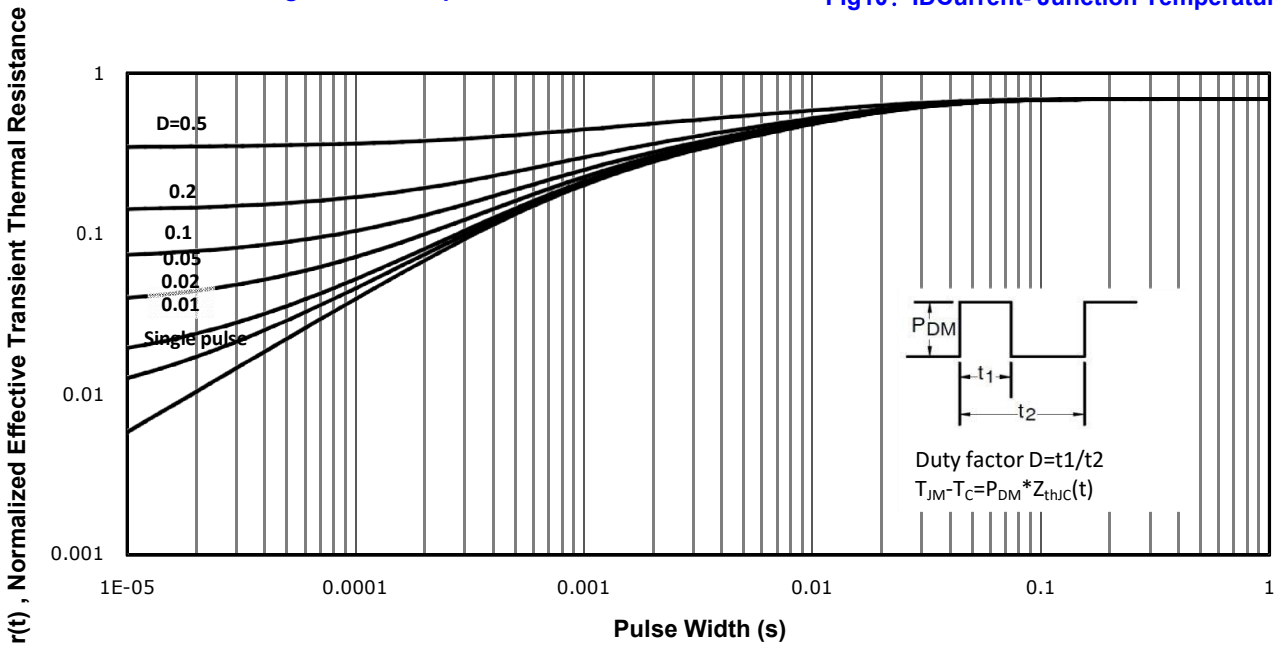


Fig11: Normalized Maximum Transient Thermal Impedance

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