KSN3072Q

30V N-Channel MOSFET

General Description

The KSN3072Q is the high cell density trenched N-ch MOSFET, which provide excellent $R_{DS}ON$ and gate charge for most of the synchronous buck converter applications. The KSN3072 meet the RoHS and Green . This device is suitable for use as a load switch or in PWM applications.

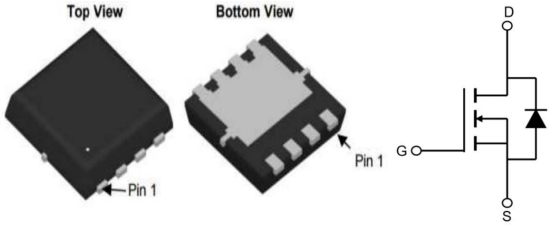
Product Summary

V_{DS} (V)	$R_{DS}(on)$ (mΩ)	I_D (A)
30	4.7 at V_{GS} = 10 V	72
	6.3 at V_{GS} = 4.5 V	50

Features

- Improved dv/dt capability
- Green Device Available
- Suit for 1.8V Gate Drive Applications

PDFN3*3 Pin Configuration



Applications

- Super Low Gate Charge
- PWM applications
- Excellent CdV/dt effect decline
- Li-Battery Protection

100% UIS TESTED!
100% ΔVds TESTED!

Absolute Maximum Ratings T_C=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
ID	Drain Current – Continuous (T_C=25℃)	72	A
	Drain Current – Continuous (T_C=100℃)	30	A
I_{DM}	Drain Current – Pulsed[1]	140	A
P_D	Power Dissipation (T_C=25℃)	50	W
	Power Dissipation – Derate above 25℃	22	W/℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	61	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	3.62	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	30	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.01	---	V/℃
I_{DSS}	Drain-Source Leakage Current	V_{DS}=30V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=30V , V_{GS}=0V , T_J=125℃	---	---	10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=20A	---	4.7	6.0	mΩ
		V_{GS}=4.5V , I_D=15A	---	6.3	8.5	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.0	1.6	2.4	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	1.6	---	mV/℃
gfs	Forward Transconductance	V_{DS}=10V , I_S=10A	---	38	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge[2,3]	V_{DS}=15V , V_{GS}=4.5V , I_D=10A	---	15.6	21	nC
Q_{gs}	Gate-Source Charge[2,3]		---	5.6	7.9	
Q_{gd}	Gate-Drain Charge[2,3]		---	6.3	9.4	
$T_{d(on)}$	Turn-On Delay Time[2,3]	V_{DS}=15V , V_{GS}=10V , R_G=3.3 Ω I_D=10A	---	5.2	7.9	ns
T_r	Rise Time[2,3]		---	10.5	16	
$T_{d(off)}$	Turn-Off Delay Time[2,3]		---	24.6	38	
T_f	Fall Time[2,3]		---	7.3	14	
C_{iss}	Input Capacitance	V_{DS}=15V , V_{GS}=0V , F=1MHz	---	1965	2460	pF
C_{oss}	Output Capacitance		---	193	276	
C_{rss}	Reverse Transfer Capacitance		---	186	255	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	72	A
I_{SM}	Pulsed Source Current		---	---	110	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1.20	V

Note :
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
3. Essentially independent of operating temperature.

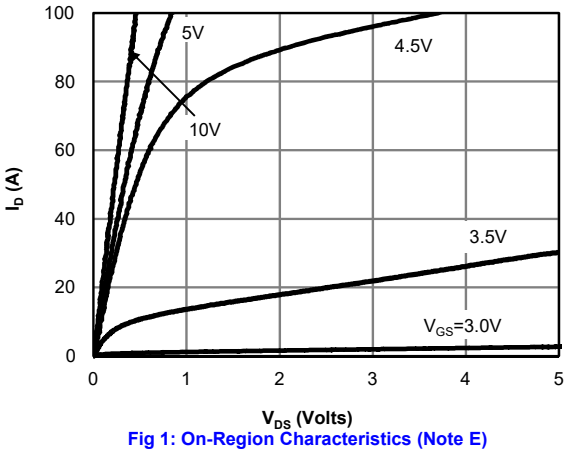


Fig 1: On-Region Characteristics (Note E)

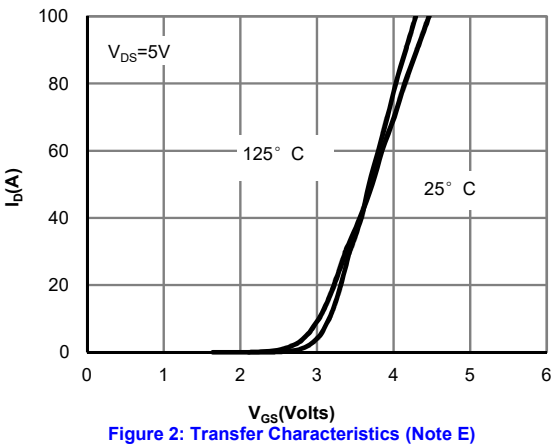


Figure 2: Transfer Characteristics (Note E)

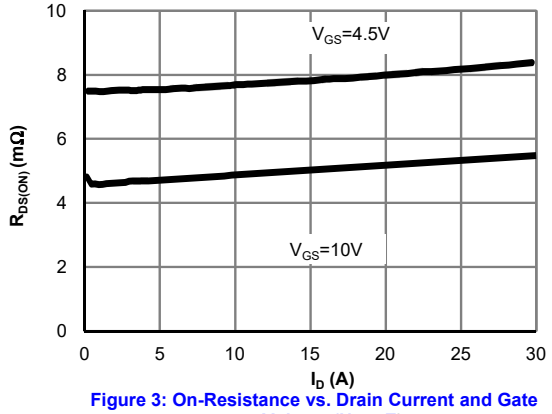


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

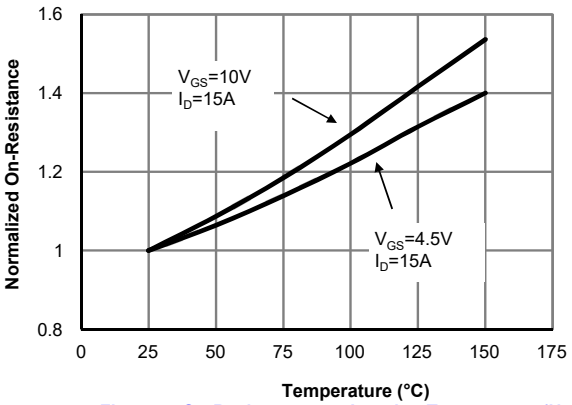


Figure 4: On-Resistance vs. Junction Temperature (Note E)

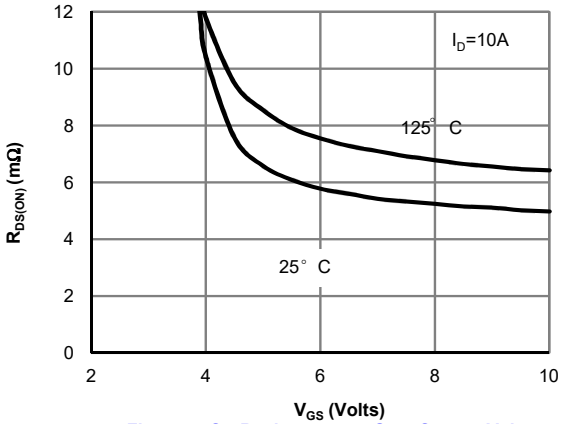


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

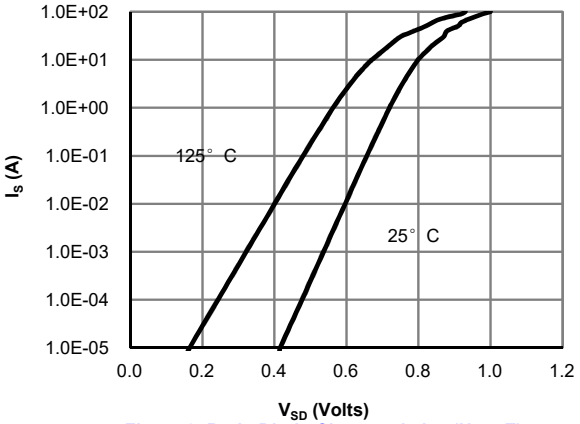


Figure 6: Body-Diode Characteristics (Note E)

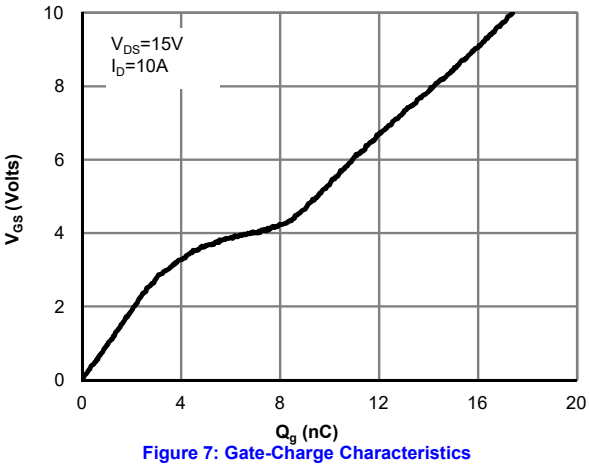


Figure 7: Gate-Charge Characteristics

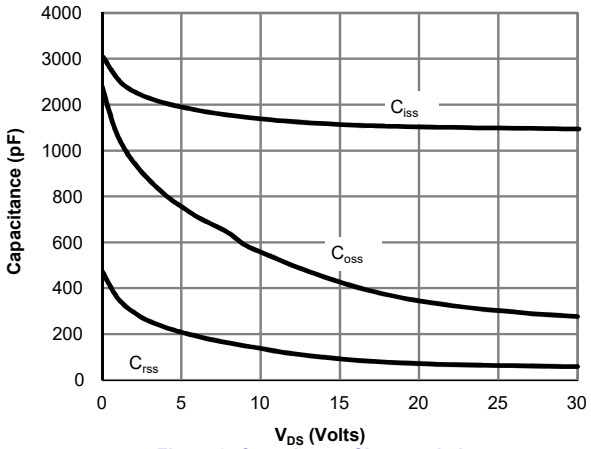


Figure 8: Capacitance Characteristics

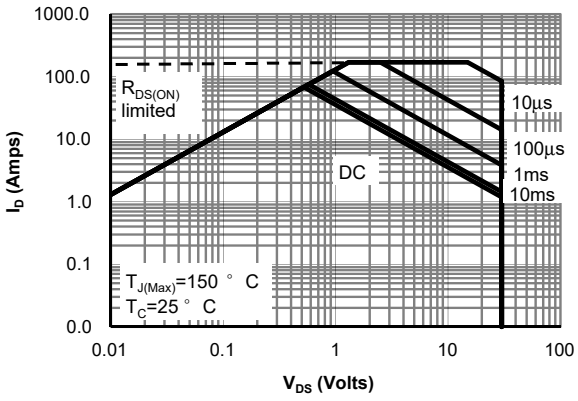


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

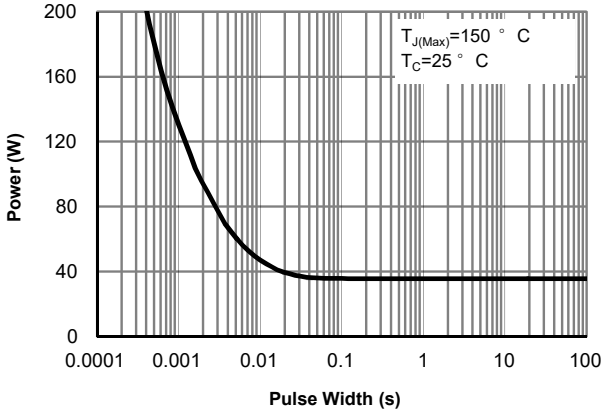


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

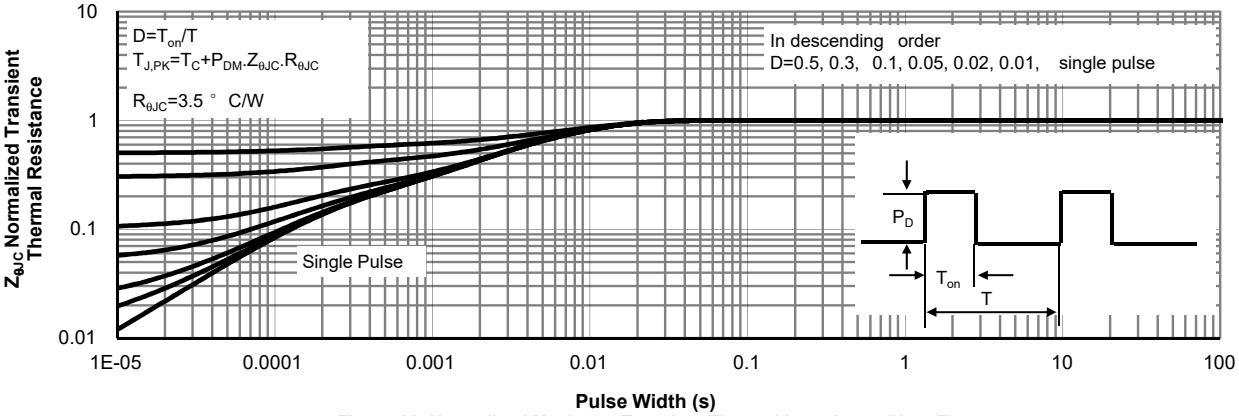


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

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