KSP4559A

N&P-Channel complementary Power MOSFET

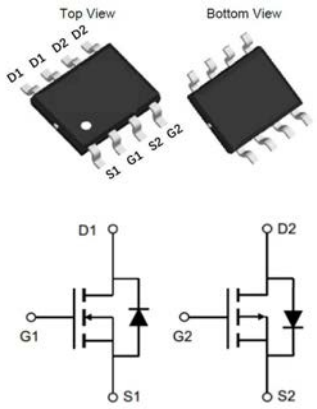
General Description

The KSP4559A uses advanced trench technology and design to provide excellent R_{DS}(ON) with low gate charge. It can be used in a wide variety of applications.

Product Summary

	V_{DS} (V)	$R_{DS(on)}$ (mΩ)	I_D (A)
N-ch	60	44 at VGS =10V	4.8
		56 at VGS =4.5V	4.2
P-ch	-60	56 at VGS = 10V	-4.0
		71 at VGS =4.5V	-3.5

SOP-8 Pin Configuration



Features

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Applications

- H-bridge
- Inverters

Absolute Maximum Ratings **Tc=25℃ unless otherwise noted**

Symbol	Parameter	N-ch Rating	P-ch Rating	Units
V_{DS}	Drain-Source Voltage	60	-60	V
V_{GS}	Gate-Source Voltage	±20	±20	V
I_D	Drain Current – Continuous (T_C=25℃)	4.8	-4.0	A
	Drain Current – Continuous (T_C=100℃)	3.2	-3.0	A
I_{DM}	Drain Current – Pulsed[1]	15	-12	A
P_D	Power Dissipation (T_C=25℃)	1.5		W
	Power Dissipation (T_C=100℃)	0.05		W/℃
T_{STG}	Storage Temperature Range	-55 to 150		℃
T_J	Operating Junction Temperature Range	-55 to 150		℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	60	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.6	℃/W

N-Channel Electrical Characteristics (TJ=25 ℃, unless otherwisenoted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	60	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=60V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=60V , V_{GS}=0V , T_J=125℃	---	---	10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=5A	---	44	52	mΩ
		V_{GS}=4.5V , I_D=4A	---	56	67	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.0	1.8	3	V
gfs	Forward Transconductance	V_{DS}=10V , I_S=6A	---	20	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	VDS=20V , VGS=10V , ID=5A	---	16	---	nC
Q_{gs}	Gate-Source Charge		---	4.2	---	
Q_{gd}	Gate-Drain Charge		---	7	---	
$T_{d(on)}$	Turn-On Delay Time	VDS=30V, RL=4.7 VGS=10V, RG=3Ω	---	8	---	ns
T_r	Rise Time		---	14	---	
$T_{d(off)}$	Turn-Off Delay Time		---	25	---	
T_f	Fall Time		---	5	---	
C_{iss}	Input Capacitance	VDS=20V , VGS=0V , F=1MHz	---	855	---	pF
C_{oss}	Output Capacitance		---	88	---	
C_{rss}	Reverse Transfer Capacitance		---	65	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	4.8	A
I_{SM}	Pulsed Source Current		---	---	9.6	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1.2	V

N-Channel Typical Electrical and Thermal Characteristics (Curves)

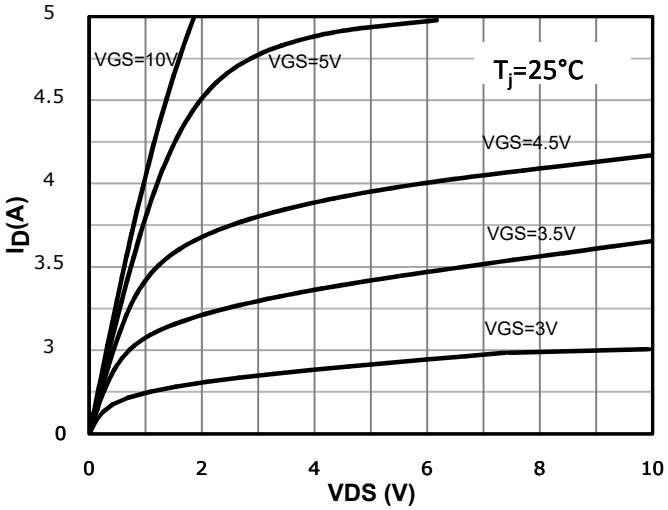


Fig1:Output Characteristics

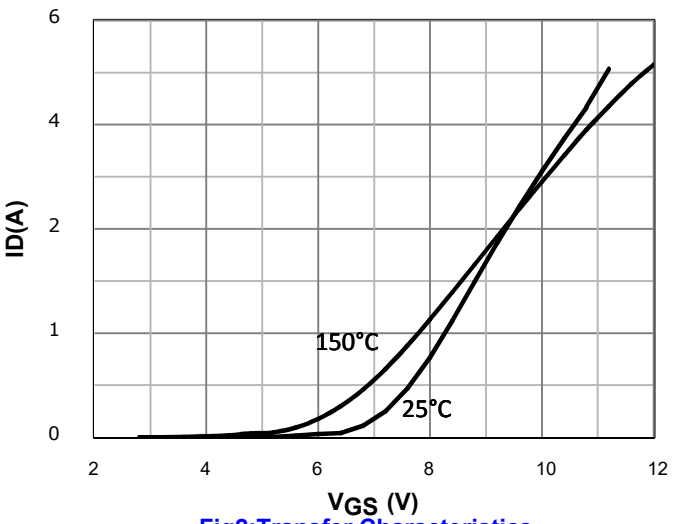


Fig2:Transfer Characteristics

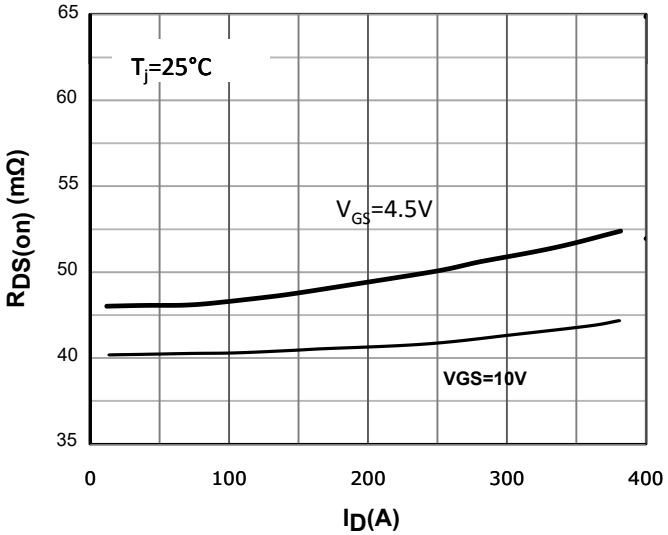


Fig3:Rdson- Drain Current

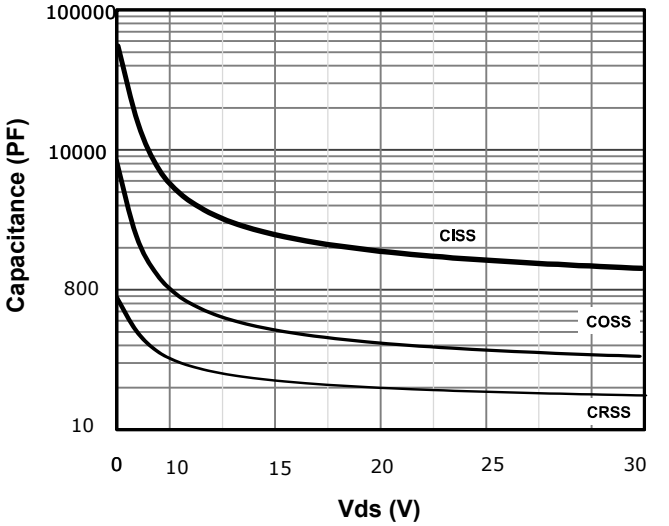


Fig4:Capacitance vs Vds

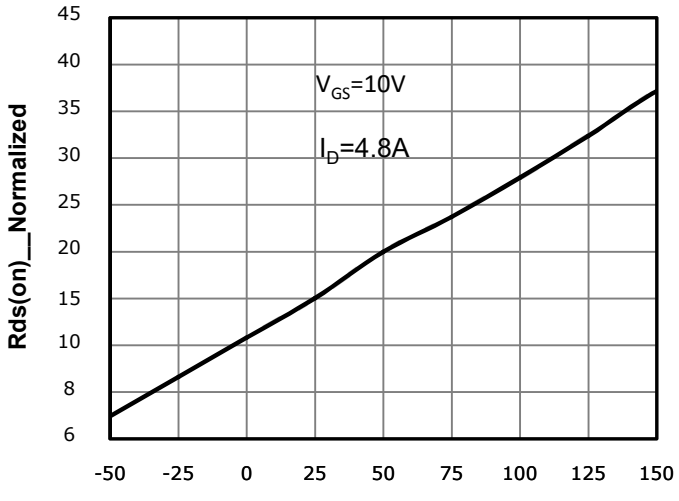


Fig5:Rds(on) vs. Temperature

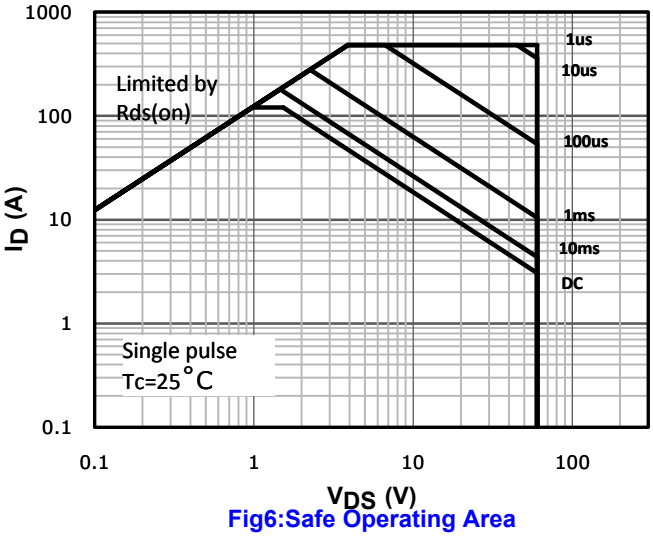


Fig6:Safe Operating Area

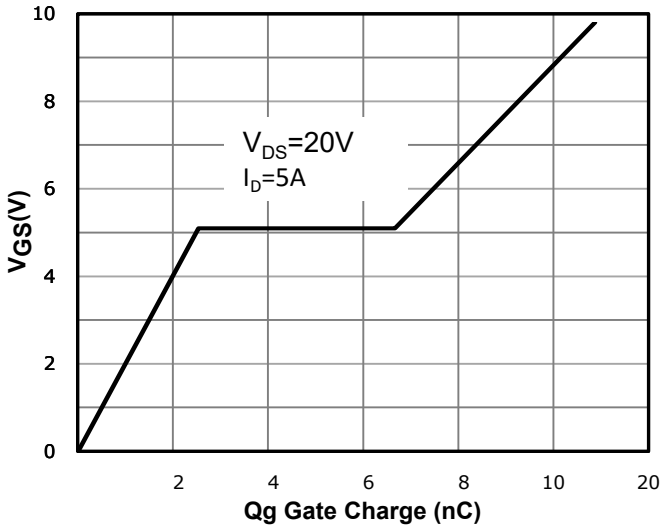


Fig7:Gate Charge

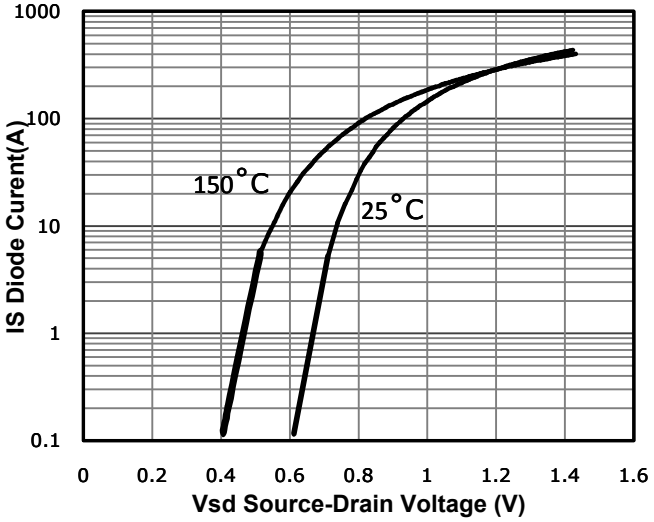


Fig8:Source- Drain Diode Forwrd

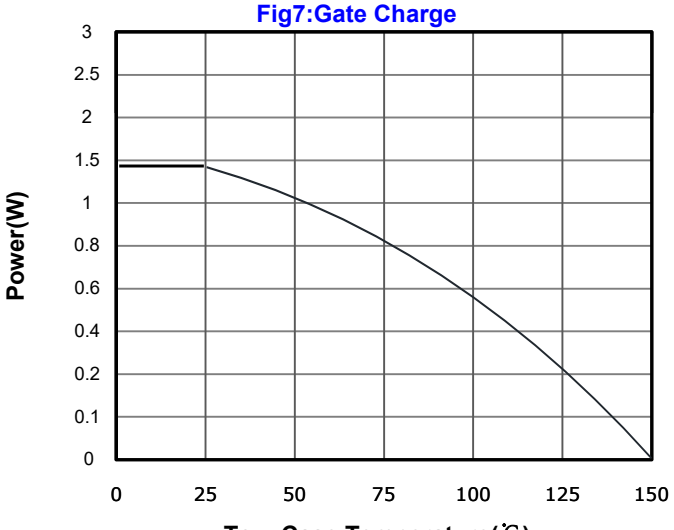


Fig9:Power Disipation

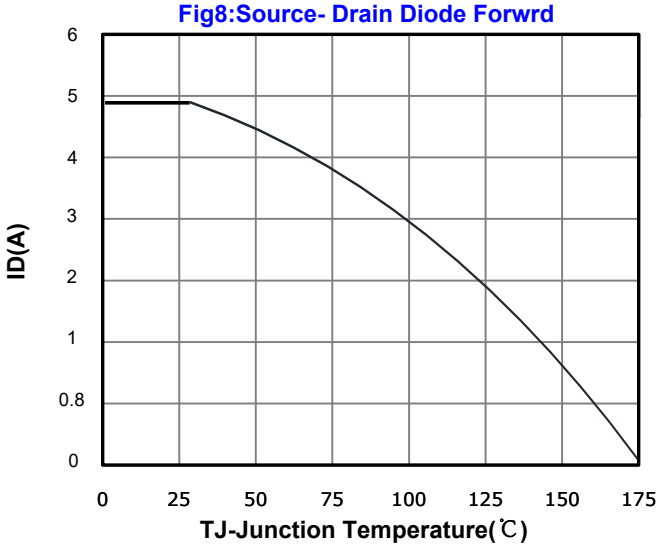


Fig10：IDCurrent- Junction Temperature

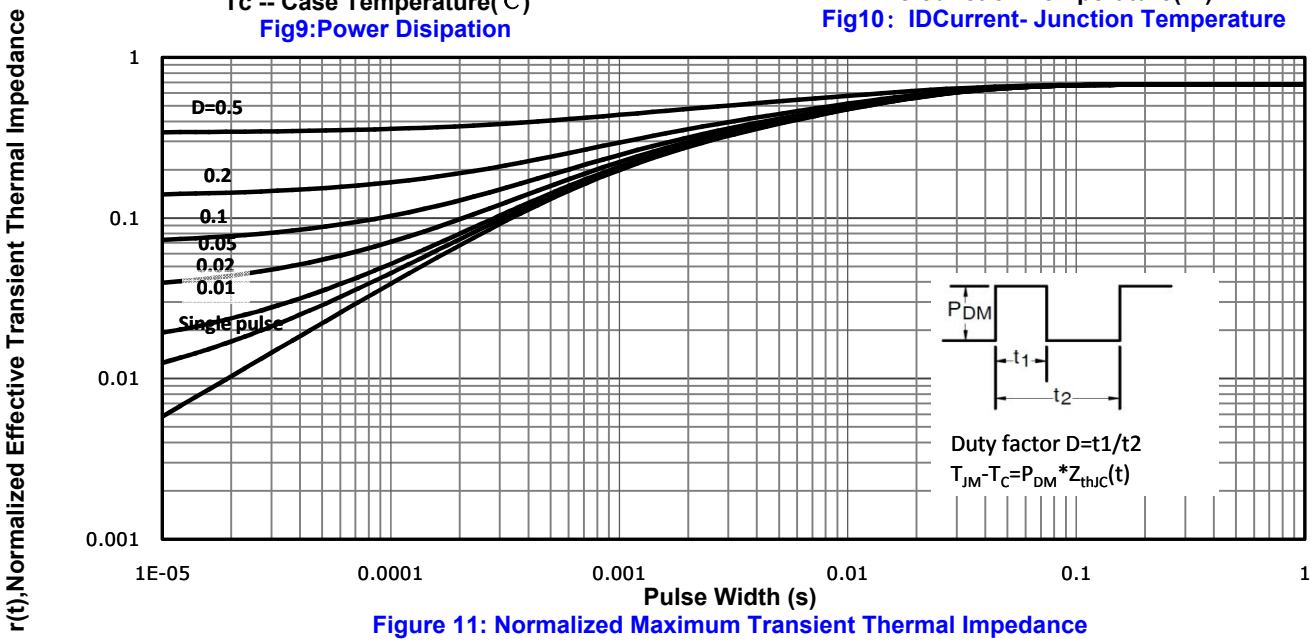


Figure 11: Normalized Maximum Transient Thermal Impedance

P-Channel Electrical Characteristics (T_J=25 ℃, unless otherwisenoted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250uA	-60	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-60V , V_{GS}=0V , T_J=25℃	---	---	-1	uA
		V_{DS}=-60V , V_{GS}=0V , T_J=125℃	---	---	-10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-10V , I_D=-5A	---	56	67	mΩ
		V_{GS}=-4.5V , I_D=-3.5A	---	71	85	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	-1.0	-1.6	-3	V
gfs	Forward Transconductance	V_{DS}=-5V , I_S=-5A	---	20	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	VDS=-30V , VGS=-10V , ID=-5A	---	10	---	nC
Q_{gs}	Gate-Source Charge		---	4.2	---	
Q_{gd}	Gate-Drain Charge		---	3.6	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DS}=-30V, R_L=3Ω V_{GS}=-10V,R_G=6Ω	---	28	---	ns
T_r	Rise Time		---	20	---	
$T_{d(off)}$	Turn-Off Delay Time		---	61	---	
T_f	Fall Time		---	7.6	---	
C_{iss}	Input Capacitance	V_{DS}=-20V , V_{GS}=0V , F=1MHz	---	1200	---	pF
C_{oss}	Output Capacitance		---	98	---	
C_{rss}	Reverse Transfer Capacitance		---	70	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	-4	A
I_{SM}	Pulsed Source Current		---	---	-8	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.2	V

Note :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. E_{AS} condition: Tj=25℃,V_{DD}=-15V,V_G=-10V,L=0.5mH,Rg=25Ω,I_{AS}=-26A

P-Channel Typical Electrical and Thermal Characteristics (Curves)

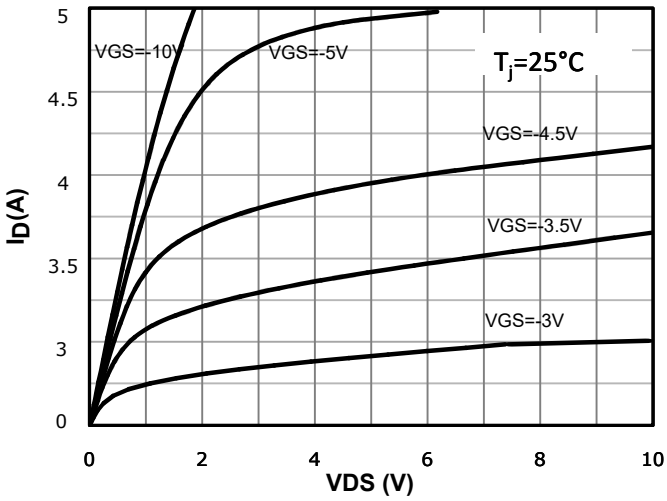


Fig1:Output Characteristics

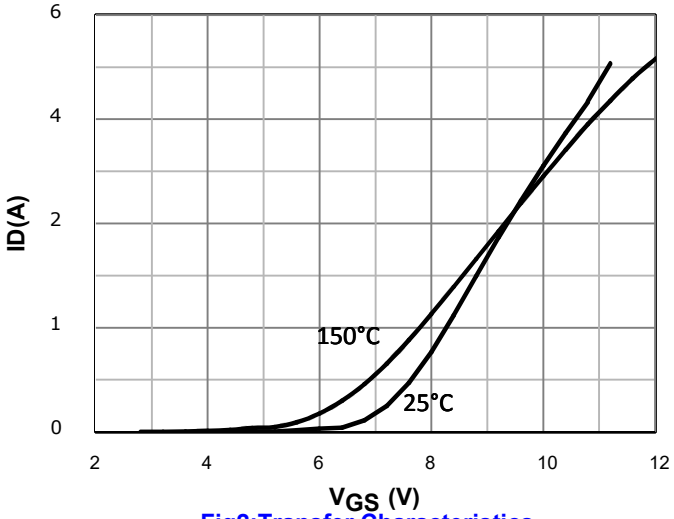


Fig2:Transfer Characteristics

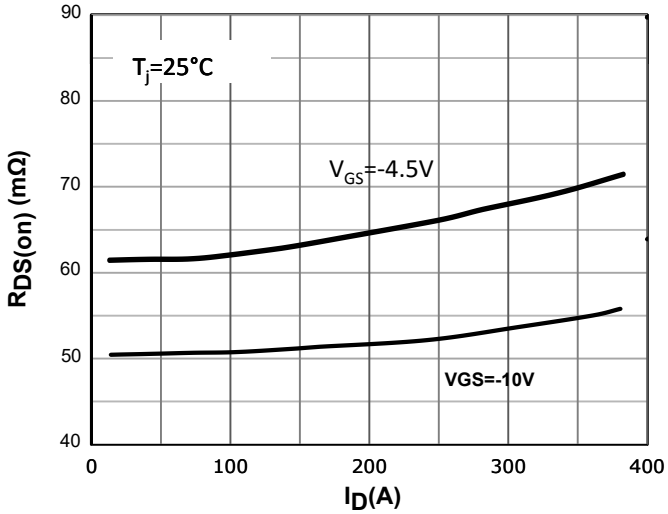


Fig3:Rdson- Drain Current

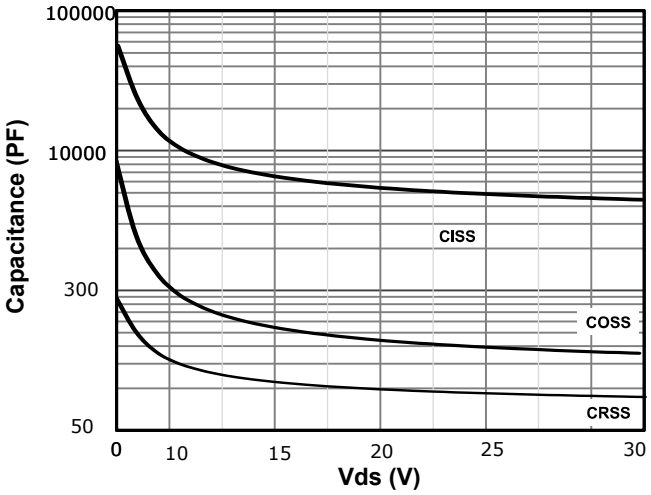


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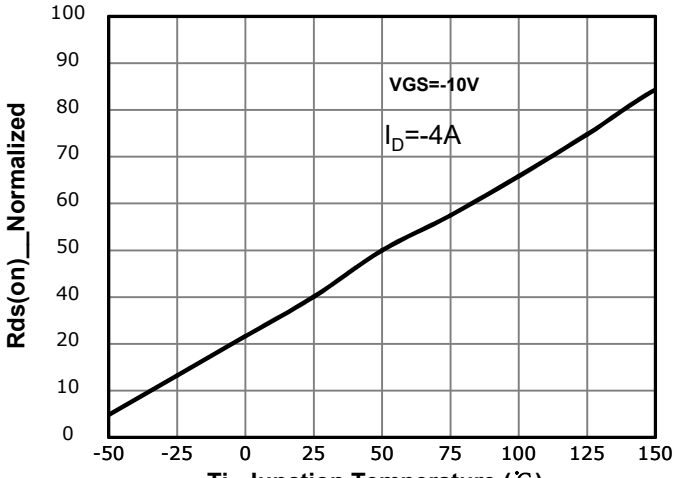


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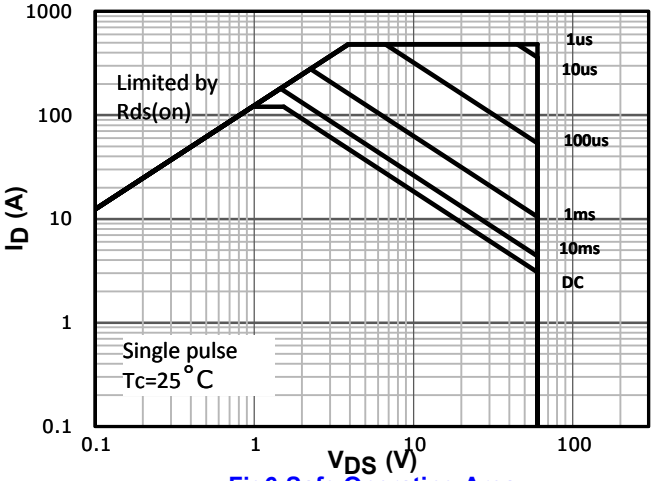


Fig6:Safe Operating Area

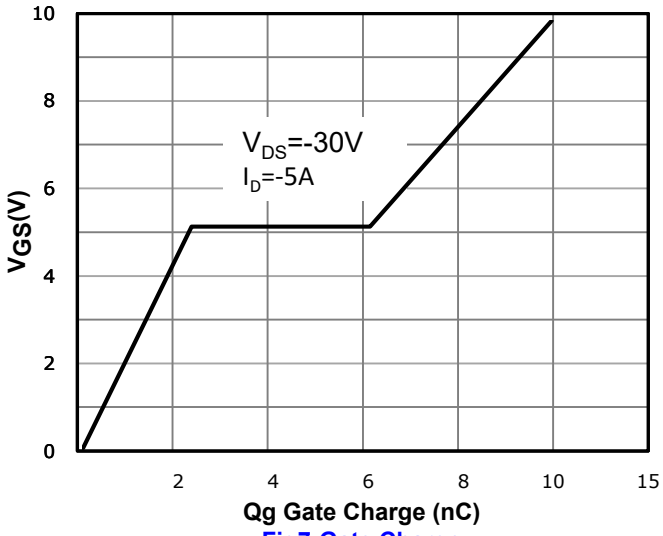


Fig7:Gate Charge

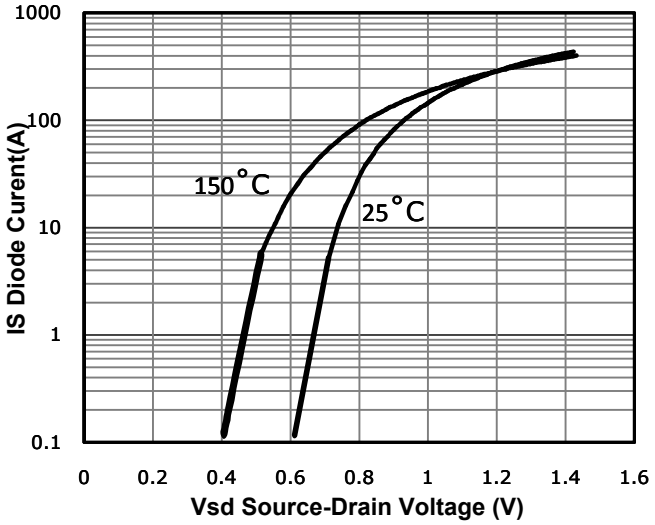


Fig8:Source- Drain Diode Forwrd

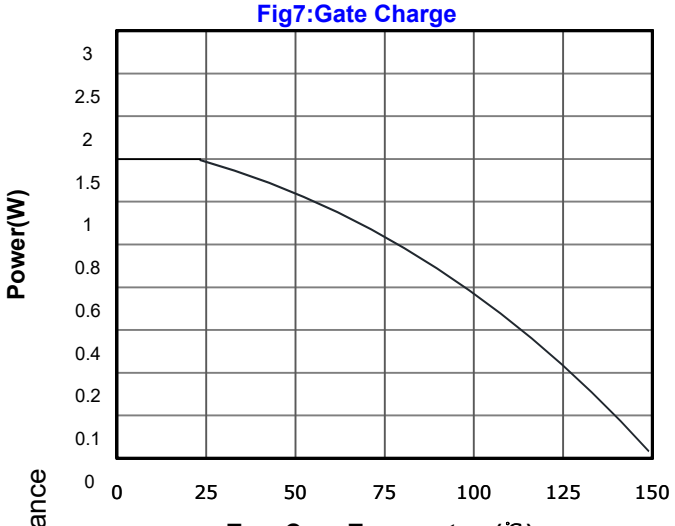


Fig9:Power Disipation

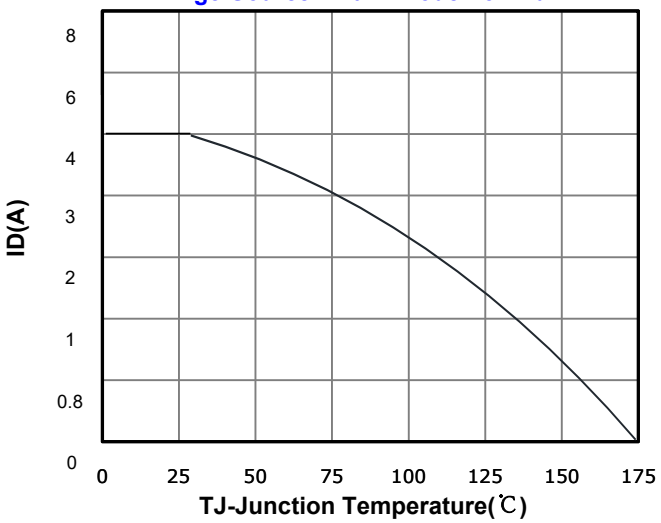


Fig10：IDCurrent- Junction Temperature

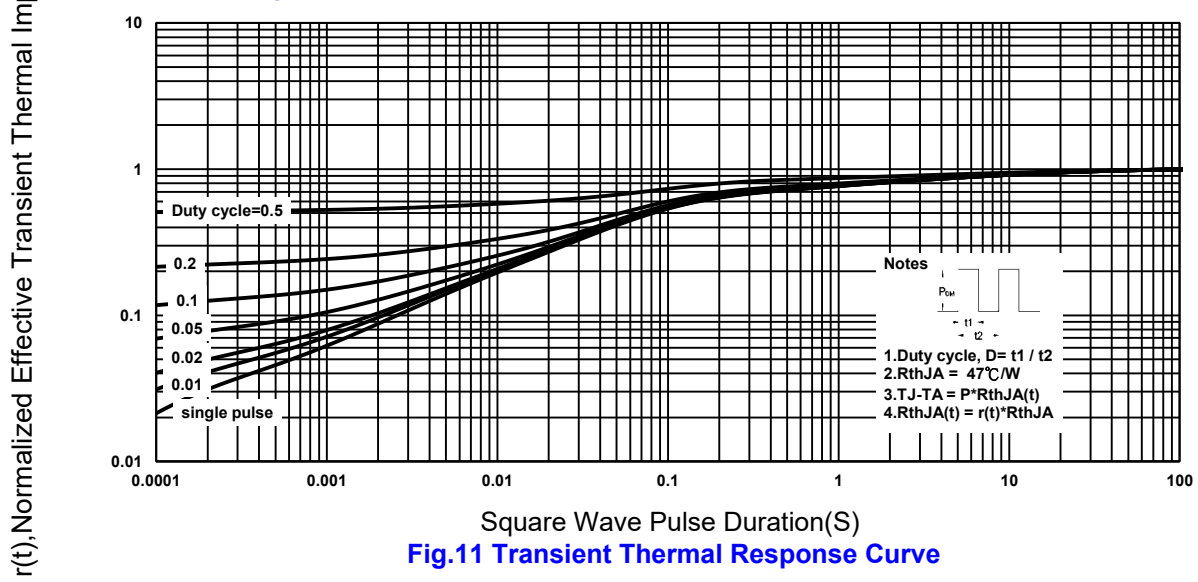


Fig.11 Transient Thermal Response Curve

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