KST3102

100V N-Channel MOSFET

General Description

The KST3102 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible onresistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

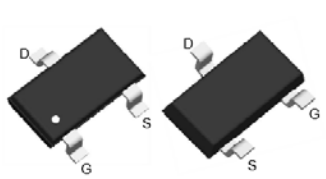
Product Summary

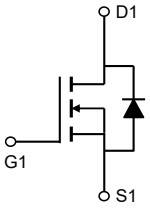
V_{DS} (V)	$R_{DS(on)}$ (mΩ)	I_D (A)
100	265 at V_{GS} = 10 V	2
	283 at V_{GS} = 4.5 V	1.6

Features

- High power and current handing capability
- Lead free product is acquired
- Surface mount package

SOT-23 Pin Configuration





Applications

- PWM applications
- Load switch Power
- management

Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current – Continuous (T_C=25℃)	2	A
	Drain Current – Continuous (T_C=100℃)	0.8	A
I_{DM}	Drain Current – Pulsed[1]	7	A
P_D	Power Dissipation (T_C=25℃)	0.3	W
	Power Dissipation (T_C=100℃)	0.01	W/℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	63	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	4.5	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	100	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=80V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=80V , V_{GS}=0V , T_J=125℃	---	---	10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±10V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=10V , I_D=2A	---	265	300	mΩ
		V_{GS}=4.5V , I_D=1A	---	280	320	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.0	1.8	3.0	V
gfs	Forward Transconductance	V_{DS}=5V , I_S=1A	---	3	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	V_{DS}=50V , V_{GS}=5V , I_D=1A	---	16	---	nC
Q_{gs}	Gate-Source Charge		---	2.2	---	
Q_{gd}	Gate-Drain Charge		---	3.1	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DS}=50V,R_L=9Ω V_{GS}=10V,R_G=3.3Ω	---	6.9	---	ns
T_r	Rise Time		---	5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	19	---	
T_f	Fall Time		---	5	---	
C_{iss}	Input Capacitance	V_{DS}=50V , V_{GS}=0V , F=1MHz	---	275	---	pF
C_{oss}	Output Capacitance		---	26	---	
C_{rss}	Reverse Transfer Capacitance		---	8	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	2	A
I_{SM}	Pulsed Source Current		---	---	6	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1.2	V

Note :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics (Curves)

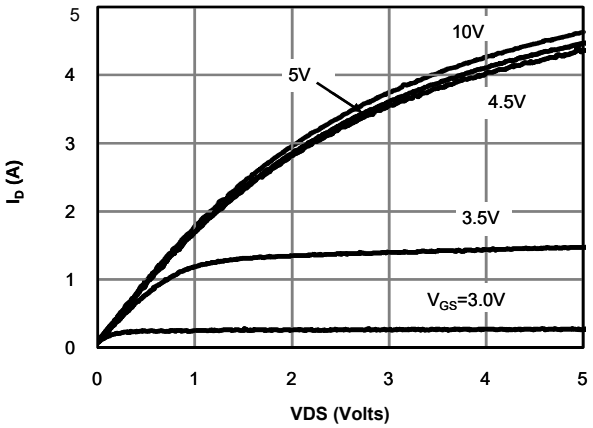


Fig 1: On-Region CCharacteristics

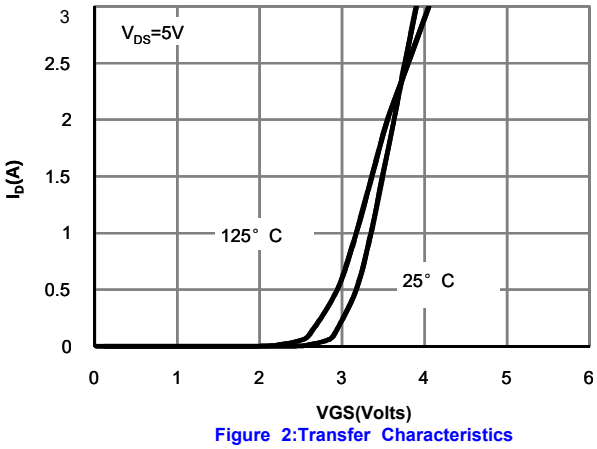


Figure 2:Transfer Characteristics

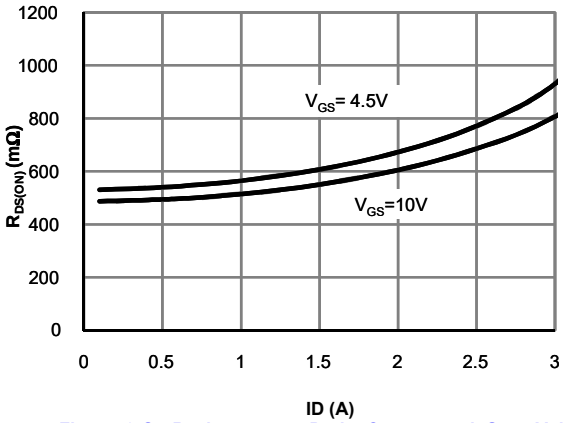


Figure 3:On-Resistance vs Drain Current and Gate Voltage

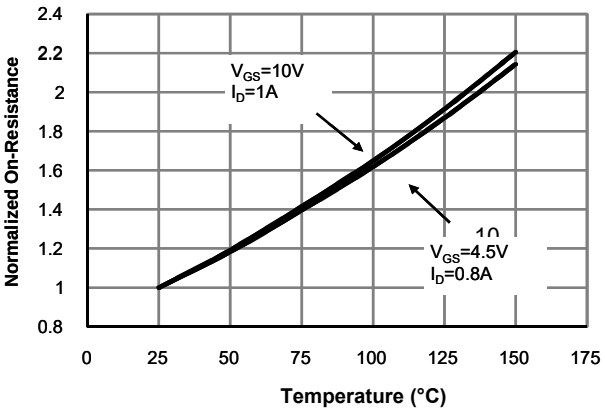


Figure 4: On-Resistance vs. Junction Temperature

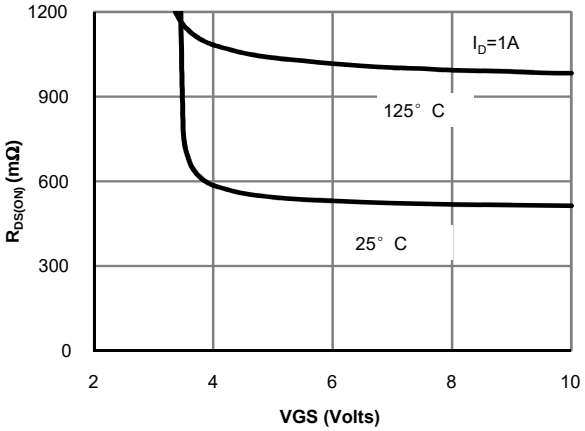


Figure 5: On-Resistance vs. Gate-Source Voltage

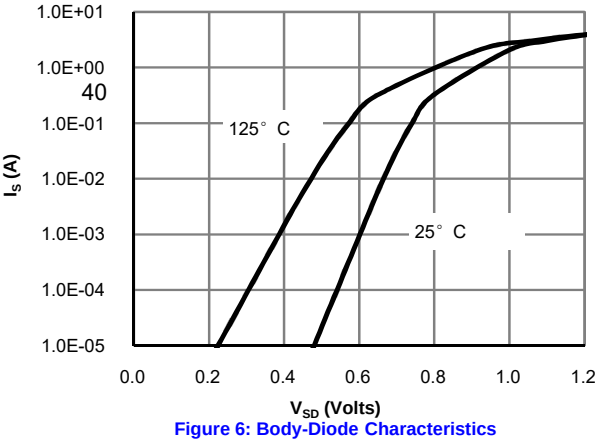


Figure 6: Body-Diode Characteristics

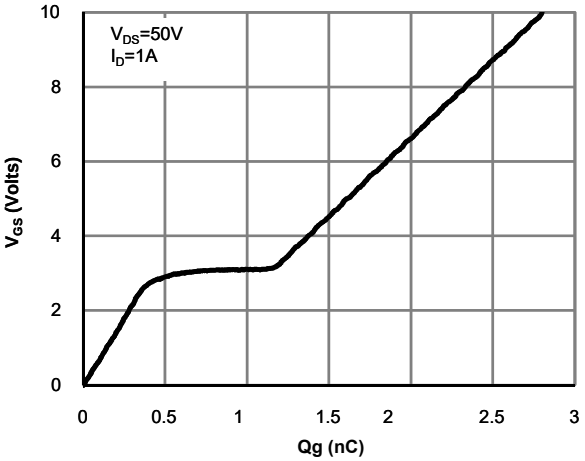


Figure 7: Gate-Charge Characteristics

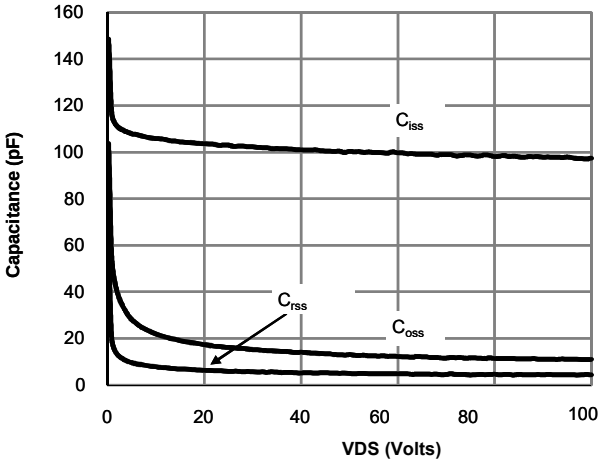


Figure8:Capacitance Characteristics

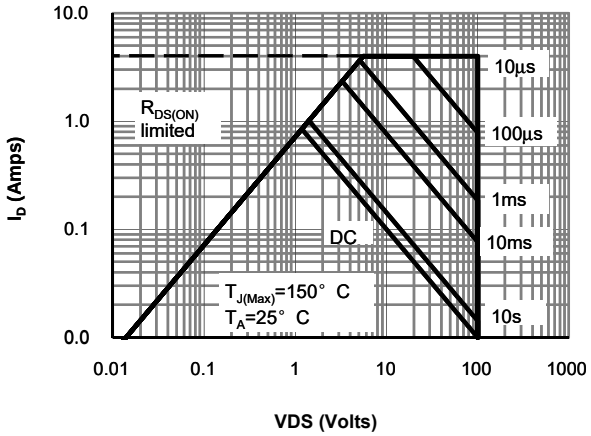


Figure 9:Maximum Forward Biased Safe Operating Area

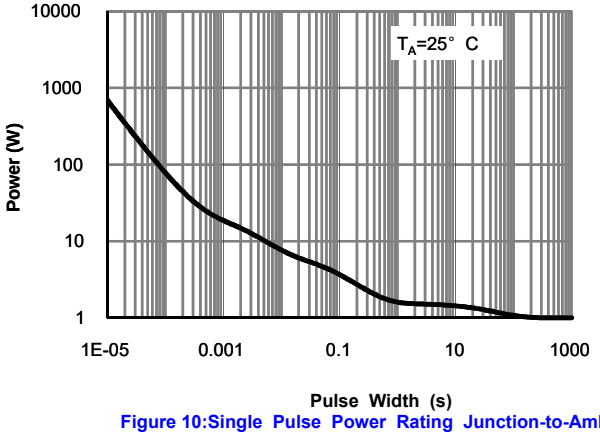


Figure 10:Single Pulse Power Rating Junction-to-Ambient

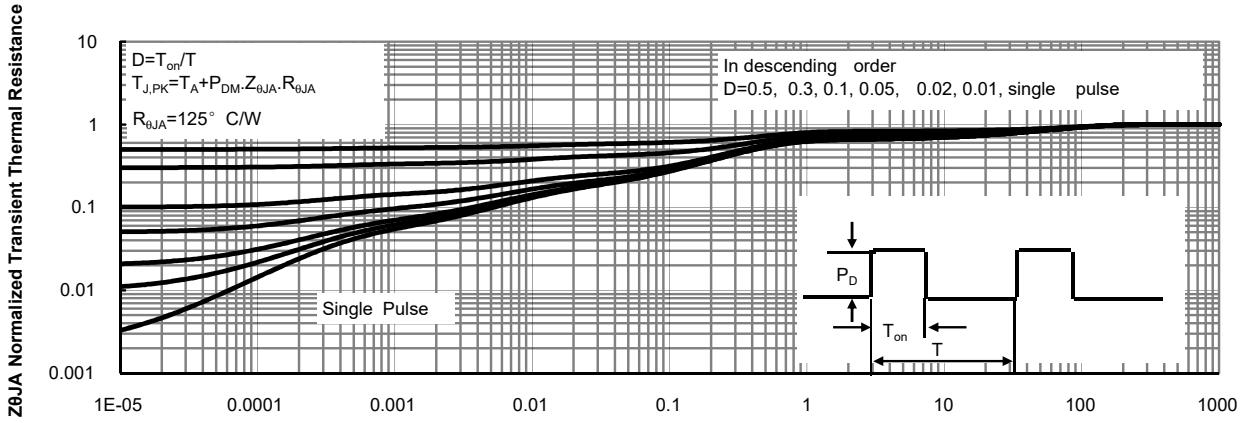


Figure 11: Normalized Maximum Transient Thermal Impedance

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