KST3445A

-40V P-Channel MOSFET

General Description

The KST3445A uses advanced trench technology and design to provide excellent R_{DS}(ON) with low gate charge .This device is well suited for use as a load switch or in PWM applications

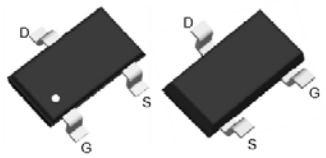
Product Summary

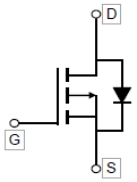
V_{DS} (V)	$R_{DS(on)}$ (mΩ)	I_D (A)
-40	39 at V_{GS} = 10 V	-4.7
	53 at V_{GS} = 4.5 V	-3.0

Features

- High power and current handing capability
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

SOT-23-3L Pin Configuration





Applications

- Battery switch
- management
- Load switch Power

Absolute Maximum Ratings **Tc=25℃ unless otherwise noted**

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	±20	V
I_D	Drain Current – Continuous (T_C=25℃)	-4.7	A
	Drain Current – Continuous (T_C=100℃)	-0.8	A
I_{DM}	Drain Current – Pulsed[1]	-15	A
P_D	Power Dissipation (T_C=25℃)	1.18	W
	Power Dissipation (T_C=100℃)	0.03	W/℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	53	℃/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.7	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	-40	---	---	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-40V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=-40V , V_{GS}=0V , T_J=125℃	---	---	10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-10V , I_D=-4.0A	---	39	51	mΩ
		V_{GS}=-4.5V , I_D=-3.0A	---	53	67	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	-1.0	-1.6	-3.0	V
gfs	Forward Transconductance	V_{DS}=-5V , I_S=-1.8A	---	15	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	V_{DS}=-20V , V_{GS}=-10V , I_D=-2.5A	---	8.4	---	nC
Q_{gs}	Gate-Source Charge		---	1.9	---	
Q_{gd}	Gate-Drain Charge		---	2.7	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DS}=-20V,I_D=-2.5A V_{GS}=-10V,R_G=1Ω	---	14	---	ns
T_r	Rise Time		---	23	---	
$T_{d(off)}$	Turn-Off Delay Time		---	17	---	
T_f	Fall Time		---	9	---	
C_{iss}	Input Capacitance	V_{DS}=-20V , V_{GS}=0V , F=1MHz	---	683	---	pF
C_{oss}	Output Capacitance		---	51	---	
C_{rss}	Reverse Transfer Capacitance		---	42	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	V_G=V_D=0V , Force Current	---	---	-1.8	A
I_{SM}	Pulsed Source Current		---	---	-5.0	A
V_{SD}	Diode Forward Voltage	V_{GS}=0V , I_S=-1A , T_J=25℃	---	---	-1.0	V

Note :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics (Curves)

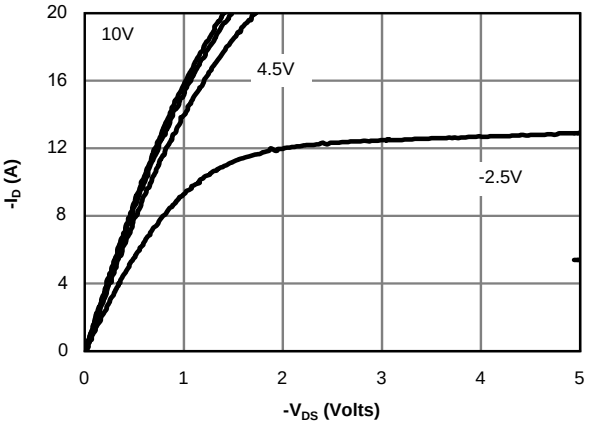


Fig 1: On-Region Characteristics

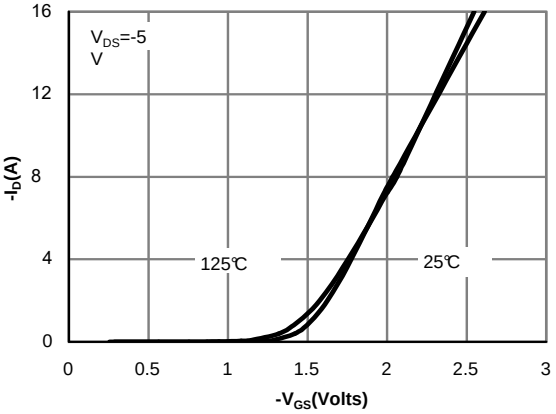


Figure 2: Transfer Characteristics

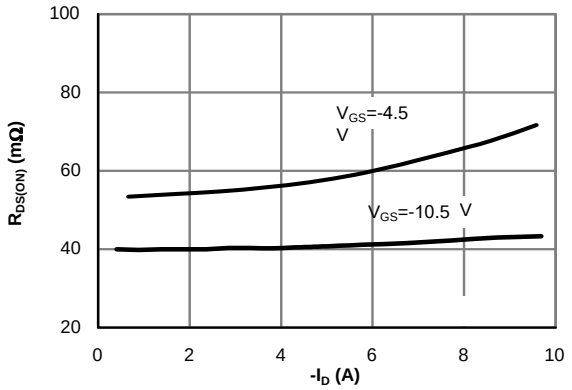


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

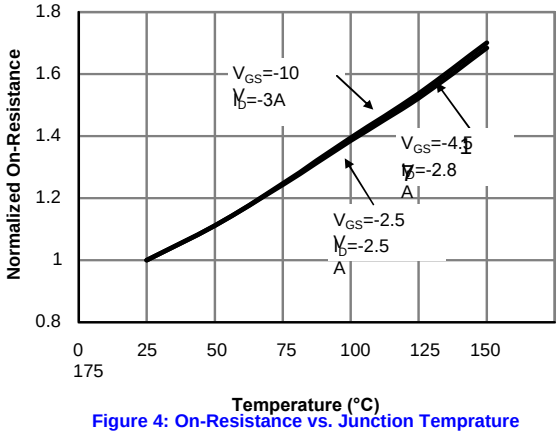


Figure 4: On-Resistance vs. Junction Temprature

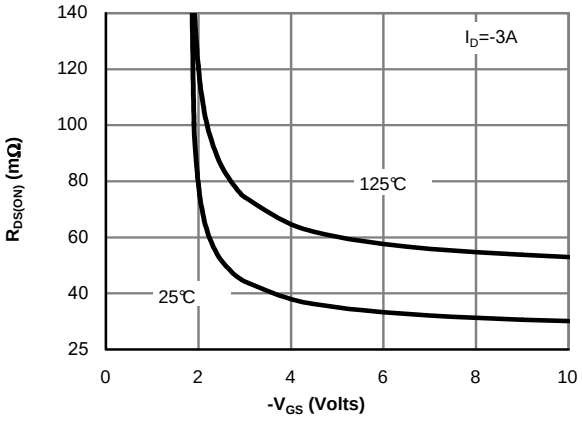


Figure 5: On-Resistance vs. Gate-Source Voltage

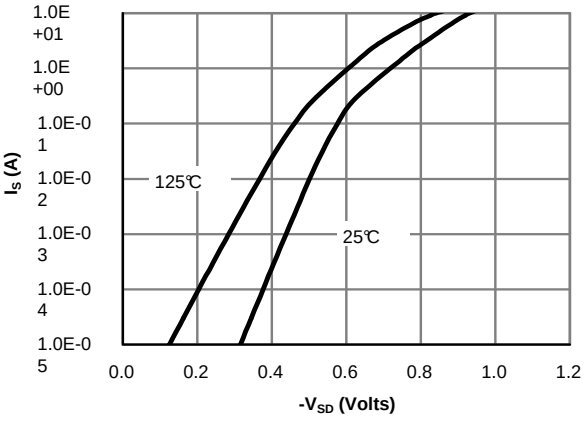


Figure 6: Body-Diode Characteristics

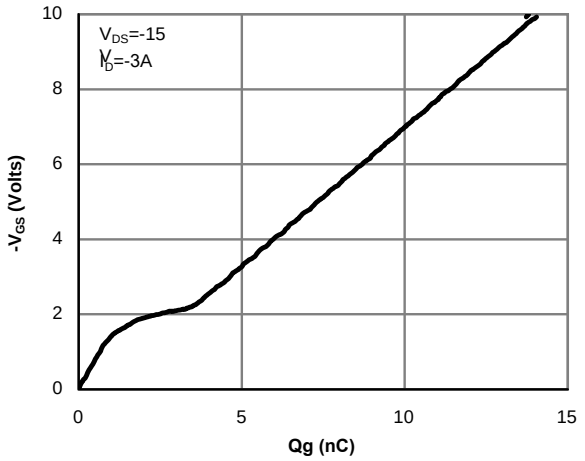


Figure 7: Gate-Charge Characteristics

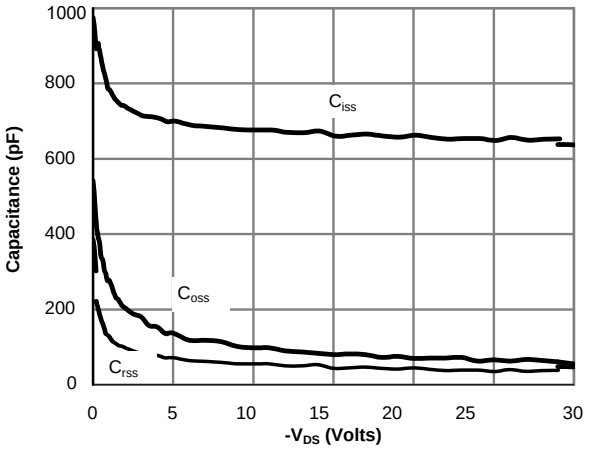


Figure 8: Capacitance Characteristics

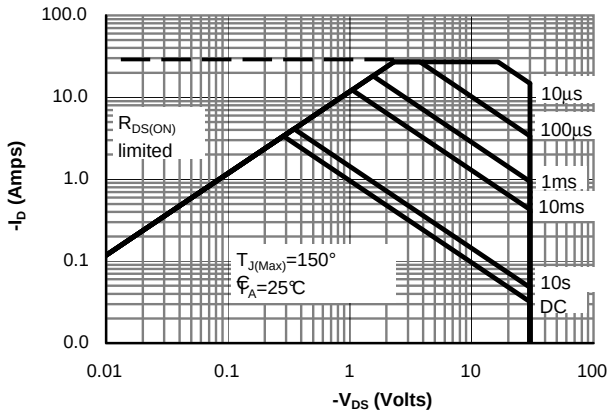


Figure 9: Maximum Forward Biased Safe Operating Area

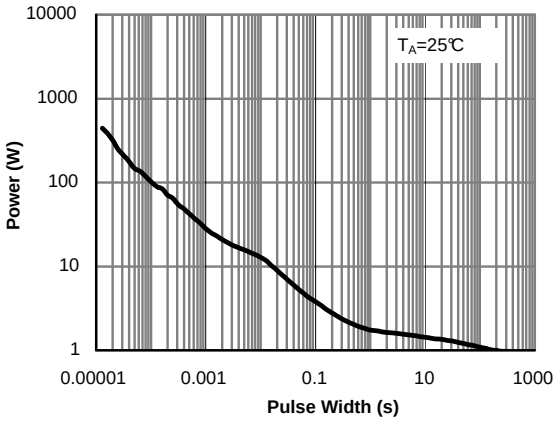


Figure 10: Single Pulse Power Rating Junction-to-Ambient

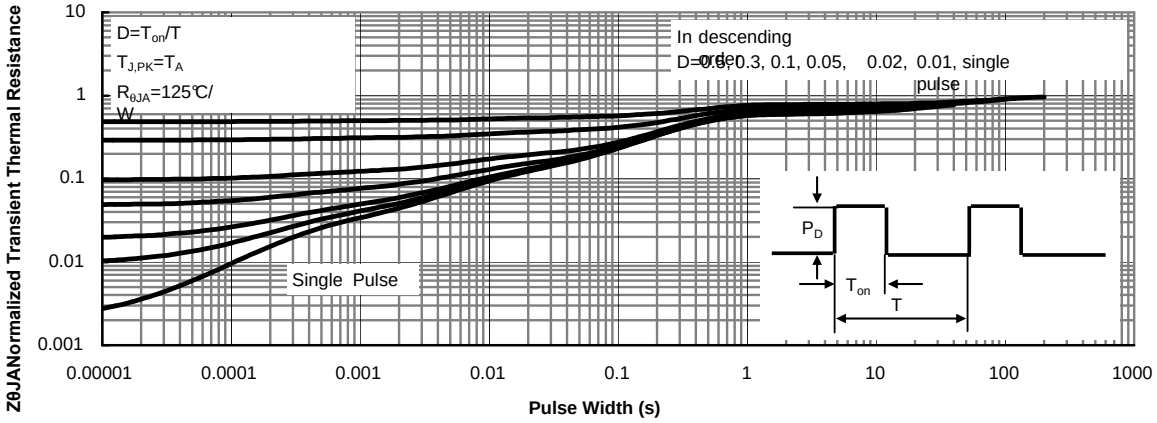


Figure 11: Normalized Maximum Transient Thermal Impedance

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